

## Outline

- Simple loop transformations
- Loop invariants based transformations
- Induction variables based transformations
- Complex loop transformations


## Simple loop transformations

Simple loop transformations are used to

- Increase performance/energy savings
and/or
- Unblock other transformations
- E.g., increase the number of constant propagations
- E.g., Extract thread-level parallelism from sequential code
- E.g., Generate vector instructions


## Loop unrolling



## Loop unrolling in LLVM: requirements

- The loop you want to unroll must be in LCSSA form


## Loop unrolling in LLVM: dependences

```
void getAnalysisUsage(AnalysisUsage &AU) const override {
    AU.addRequired<AssumptionCacheTracker>();
    AU.addRequired<DominatorTreeWrapperPass>();
    AU.addRequired<LoopInfoWrapperPass>();
    AU.addRequired<ScalarEvolutionWrapperPass>();
    AU.addRequired<TargetTransformInfoWrapperPass>();
}
```


## Loop unrolling in LLVM: headers

```
#include "llvm/Analysis/OptimizationRemarkEmitter.h"
#include "llvm/IR/Dominators.h"
#include "llvm/Transforms/Utils/LoopUtils.h"
#include "llvm/Transforms/Utils/UnrollLoop.h"
#include "llvm/Analysis/AssumptionCache.h"
#include "llvm/Analysis/ScalarEvolution.h"
#include "llvm/Analysis/ScalarEvolutionExpressions.h"
#include "llvm/Analysis/TargetTransformInfo.h"
```


## Loop unrolling in LLVM

## Get the results of the required analyses

```
auto& LI = getAnalysis<LoopInfoWrapperPass>().getLoopInfo();
auto& DT = getAnalysis<DominatorTreeWrapperPass>().getDomTree();
auto& SE = getAnalysis<ScalarEvolutionWrapperPass>().getSE();
auto& AC = getAnalysis<AssumptionCacheTracker>().getAssumptionCache(F);
const auto &TTI = getAnalysis<TargetTransformInfoWrapperPass>().getTTI(F);
```


## Fetch a loop

```
for (auto i : LI){
    auto loop = &*i;
}
```

void getAnalysisUsage(AnalysisUsage \&AU) const override \{
AU.addRequired<AssumptionCacheTracker>();
AU. addRequired<DominatorTreeWrapperPass>();
AU.addRequired<LoopInfoWrapperPass>();
AU.addRequired<ScalarEvolutionWrapperPass>();
return ;
\}

```
auto& LI = getAnalysis<LoopInfoWrapperPass>().getLoopInfo();
auto& DT = getAnalysis<DominatorTreeWrapperPass>().getDomTree();
auto& SE = getAnalysis<ScalarEvolutionWrapperPass>().getSE();
auto& AC = getAnalysis<AssumptionCacheTracker>().getAssumptionCache(F);
const auto &TTI = getAnalysis<TargetTransformInfoWrapperPass>().getTTI(F);
```


## Loop unrolling in LLVM: API

UnrollLoopOptions UIS,
ULO. Count $=2$;
ULO. Force $=$ false;
ULO.Runtime = false;
ULO.AllowExpensiveTripCount = true;
ULO.UnrollRemainder = false;
ULO. ForgetAllSCEV = true;

## Unrolling factor

## auto tripCount = SE.getSmallConstantTripCount(loop);

It is 0 , or the number of iterations per invocation

## Loop to unroll

```
auto unrolled = UnrollLoop( Unrolling
loop, ULO, ↔ options
loop, ULO,
auto\& LI = getAnalysis<LoopInfoWrapperPass>().getLoopInfo();
auto\& DT = aetAnalvsis<DominatorTreeWrannerPass>(). aetDomTree();
auto\& SE = getAnalysis<ScalarEvolutionWrapperPass>().getSE();
auto\& \(A C=\) getAnalysis<AssumptionCachelracker>().getAssumptionCache(F);
const auto \&TTI = getAnalysis<TargetTransformInfoWrapperPass>().getTTI(F);


\section*{Loop unrolling in LLVM: result}
```

switch (unrolled){
case LoopUnrollResult::FullyUnrolled :
errs() << " Fully unrolled\n";
return true ;
case LoopUnrollResult::PartiallyUnrolled
errs() << " Partially unrolled\n";
return true ;
case LoopUnrollResult::Unmodified :
errs() << " Not unrolled\n";
break ;
default:
abort();
}

```
auto unrolled = UnrollLoop(
    loop, ULO,
    \&LI, \&SE, \&DT, \&AC, \&TTI, \&ORE, true
    );

\section*{Loop unrolling in LLVM: example}


CFG for 'main' function


\section*{Loop unrolling in LLVM: Demo}
- Detail: Loops/README
- Pass: Loops/lvm/7
- C program: Loops/code/12
- C program: Loops/code/0

\section*{Loop unrolling in LLVM: example 2}


CFG for 'main' function


There is still the same amount of loop overhead!

\section*{Loop unrolling in LLVM: the runtime checks}
```

UnrollLoopOptions ULO;
ULO.Count = 2;
ULO.Force = false;
ULO.Runtime = false; }\longrightarrow\mathrm{ true
ULO.AllowExpensiveTripCount = true;
ULO.UnrollRemainder = false;
ULO.ForgetAllSCEV = true;

```

Loop unrolling in LLVM: example 3

auto \(n=0\)
for (;n<i_mul; n+=4)\{ Body Body
Body Body
\(\}\)
for (auto \(m=0 ; \mathrm{m}<\) i_rest \(m++\) ) \{ Body
\(\}\)
Runtime checks

\section*{Loop unrolling in LLVM: API}
```

auto\& LI = getAnalysis<LoopInfoWrapperPass>().getLoopInfo();
auto\& DT = getAnalysis<DominatorTreeWrapperPass>().getDomTree();
auto\& SE = getAnalysis<ScalarEvolutionWrapperPass>().getSE();
auto\& AC = getAnalysis<AssumptionCacheTracker>().getAssumptionCache(F);
const auto \&TTI = getAnalysis<TargetTransformInfoWrapperPass>().getTTI(F);

```
auto unrolled = UnrollLoop(
    loop, ULO,
    \&LI, \&SE, \&DT, \&AC, \&TTI, \&ORE, true
    OptimizationRemarkEmitter ORE(\&F);

\section*{Loop peeling}


\section*{Loop peeling in LLVM}
- API \#include "llvm/Transforms/Utils/LoopPeel.h"
auto peeled = peelLoop(
loop, peelingCount,
\&LI, \&SE, \&DT, \&AC, true);
- No trip count
- No flags
- (almost) always possible
- To check if you can peel, invoke the following API: bool canPeel(Loop *loop)

\section*{Loop peeling in LLVM: example}


\section*{Fetching analyses outputs from a module pass}

\section*{- From a function pass}
```

auto\& LI = getAnalysis<LoopInfoWrapperPass>().getLoopInfo();
auto\& DT = getAnalysis<DominatorTreeWrapperPass>().getDomTree();
auto\& SE = getAnalysis<ScalarEvolutionWrapperPass>().getSE();
auto\& AC = getAnalysis<AssumptionCacheTracker>().getAssumptionCache(F);

```

\section*{- From a module pass}
```

auto\& LI = getAnalysis<LoopInfoWrapperPass>(F).getLoopInfo();
auto\& DT = getAnalysis<DominatorTreeWrapperPass>(F).aetDomTree();
auto\& SE = getAnalysis<ScalarEvolutionWrapperPass>(F).getSE();
auto\& AC = getAnalysis<AssumptionCacheTracker>().getAssumptionCache(F);

```

\section*{Outline}
- Simple loop transformations
- Loop invariants based transformations
- Induction variables based transformations
- Complex loop transformations

\section*{Optimizations in small, hot loops}
- Most programs: \(90 \%\) of time is spent in few, small, hot loops while ()\{
statement 1
statement 2
statement 3
\}
- Deleting a single statement from a small, hot loop might have a big impact (100 seconds -> 70 seconds)

\section*{Loop example}

1: if \((N>5)\{k=1 ; z=4 ;\}\)

2: else \(\{k=2 ; z=3 ;\}\)
do \{
3: \(a=1\);
4: \(y=x+N\);
5: \(\quad b=k+z\);
6: c = a * 3;
7: if \((N<0)\{\)
8: \(m=5\);
9: break; \}
10: x++;
11:\} while \((x<N)\);
- Observation: each statement in that loop will contribute to the program execution time
- Idea: what about moving statements from inside a loop to outside it?
- Which statements can be moved outside our loop?
- How to identify them automatically? (code analysis)
- How to move them? (code transformation)

\section*{Hoisting code}
- In order to "hoist" a loop-invariant computation out of a loop, we need a place to put it
- We could copy it to all immediate predecessors of the loop header...
for (auto pBB : predecessors(H))\{
\(\mathrm{p}=\mathrm{pBB}\)->getTerminator();
inv->moveBefore(p);
\}
Is it correct?

- ...But we can avoid code duplication (and bugs) by taking advantage of loop normalization that guarantees the existence of the pre-header

\section*{Hoisting code}
- In order to "hoist" a loop-invariant computation out of a loop, we need a place to put it
- We could copy it to all immediate predecessors of the loop header...
pBB = loop->getLoopPreheader();

- ...but we can avoid code duplication (and bugs)
by taking advantage of loop normalization that guarantees the existence of the pre-header

\section*{Can we hoist all invariant instructions of a loop L in the pre-header of \(L\) ?}


\section*{Hoisting conditions}

\section*{Loop invariant code motion}
- For a loop-invariant definition
(d) \(t=x\) op \(y\)
- Assuming no SSA, we can hoist d into the loop's pre-header if
1. d dominates all loop exits at which \(t\) is live-out, and
2. there is only one definition of \(t\) in the loop, and
3. \(t\) is not live-out of the pre-header


\section*{Hoisting conditions}

\section*{Loop invariant code motion}
- For a loop-invariant definition
(d) \(t=x\) op \(y\)
- Assuming no SSA, we can hoist d into the loop's pre-header if
1. d dominates all loop exits at which \(t\) is live-out, and
2. there is only one definition of \(t\) in the loop, and
3. \(t\) is not live-out of the pre-header


\section*{Hoisting conditions}

\section*{Loop invariant code motion}
- For a loop-invariant definition
(d) \(t=x\) op \(y\)
- Assuming no SSA, we can hoist d into the loop's pre-header if
1. d dominates all loop exits at which \(t\) is live-out, and
2. there is only one definition of \(t\) in the loop, and
3. \(t\) is not live-out of the pre-header


\section*{Hoisting conditions}

\section*{Loop invariant code motion}
- For a loop-invariant definition
(d) \(t=x\) op \(y\)
- Assuming SSA, we can hoist d into the loop's pre-header if
1. ddominates allloop-exits at whicht is live-out, and
2. there is only one definition of \(t\) in the loop, and
3. \(t\) is not live-out of the pre-header

\section*{Hoisting conditions}

\section*{Loop invariant code motion}
- For a loop-invariant definition
(d) \(\mathrm{t}=\mathrm{xopy}\)
- Assuming SSA, we can hoist d into the loop's pre-header if t is not live-out of the pre-header

\section*{Hoisting conditions}

\section*{Loop invariant code motion}
- For a loop-invariant definition
(d) \(t=\operatorname{load} X\)
- Assuming SSA, we can hoist d into the loop's pre-header if ??

\section*{Outline}
- Simple loop transformations
- Loop invariants based transformations
- Induction variables based transformations
- Complex loop transformations

\section*{Loop example}

1: if \((N>5)\{k=1 ; z=4 ;\}\)
2: else \(\{k=2 ; z=3 ;\}\)
Assuming a,b,c,m are used after our code

Do we have to execute 4 for every iteration?

Do we have to execute 10 for every iteration?
11:\} while \((x<N)\);

\section*{Loop example}


\section*{Loop example}


\section*{Loop example}


\section*{Loop example}


\section*{Loop example}


\section*{Is the code transformation worth it?}

1: if \((N>5)\{k=1 ; z=4 ;\}\)
2: else \(\{k=2 ; z=3 ;\}\)
A: \(y=N ; \operatorname{tmp}=2 * N\);


\section*{... and after Loop Invariant Code Motion ...}
\[
\begin{aligned}
& \text { 1: if }(N>5)\{k=1 ; z=4 ;\} \\
& 2: \text { else }\{k=2 ; z=3 ;\} \\
& A: y=N ; \operatorname{tmp}=2^{*} N ; \\
& 3: a=1 ; \\
& 5: b=k+z ; \\
& 6: c=a * 3 ;
\end{aligned}
\]


1: if \((N>5)\{k=1 ; z=4 ;\}\)
2: else \(\{k=2 ; z=3 ;\}\)

\section*{... and with a better Loop Invariant Code Motion ...}

1: if \((N>5)\{k=1 ; z=4 ;\}\)
2: else \(\{k=2 ; z=3 ;\}\)
1: if \((N>5)\{k=1 ; z=4 ;\}\)
2: else \(\{k=2 ; z=3 ;\}\)
A :y=N;tmp=2*N;
3 : \(a=1\);
\(5: b=k+z ;\)
6: \(\mathrm{c}=\mathrm{a} * 3\);
7: if \((N<0)\{\)
8: \(m=5\);
\}

do
3: \(a=1\);
4: \(y=x+N\);
5: \(\quad b=k+z ;\)
6: \(c=a * 3\);
8: \(\quad \mathrm{m}=5\);
9: break;
10: \(x++\);
11:\} while ( \(x\) < N);

\section*{... and after dead code elimination ...}

1: if \((N>5)\{k=1 ; z=4 ;\}\)
2: else \(\{k=2 ; z=3 ;\}\)

3 :a=1;
5 :b=k+z;
6: \(\mathrm{c}=\mathrm{a} * 3\);
7: if \((N<0)\{\)
8: m=5;
\}

Assuming \(a, b, c, m\) are used after our code

1: if \((N>5)\{k=1 ; z=4 ;\}\)
2: else \(\{k=2 ; z=3 ;\}\)


\section*{Induction variable elimination}
- Suppose we have a loop variable
- i initially set to \(\mathrm{i}_{0}\); each iteration \(\mathrm{i}=\mathrm{i}+1\)
- and a variable that linearly depends on it
- \(x=i^{*} c_{1}+c_{2}\)
- We can

Loop invariants
- Initialize \(x=i_{0}{ }^{*} c_{1}+c_{2}\)
- Increment \(x\) by \(c_{1}\) each iteration

\section*{Is it faster?}
\begin{tabular}{|l|}
\hline 1: \(i=i_{p}\) \\
2: \(d o l\) \\
\(3: \quad i=i+1 ;\) \\
\(\quad \ldots\) \\
A: \(x=i^{*} c_{1}+c_{2}\) \\
B: \(\}\) while \((i<\) maxl \() ;\) \\
\hline
\end{tabular}


On some hardware, adds are faster than multiplies
- Strength reduction

\section*{Induction variable elimination: step 1}

Run induction variable identification
(1)Iterate over IVs
\[
k=j * c 1+c 2
\]
- where the IV \(\mathrm{j}=(\mathrm{i}, \mathrm{a}, \mathrm{b})\), and
- this is the only def of \(k\) in the loop, and
- there is no def of \(i\) between the def of \(j\) and the def of \(k\)
\[
\begin{array}{|l|}
\hline i=\ldots \\
\ldots \\
j=i \ldots \\
\ldots \\
k=j \ldots \\
\hline
\end{array}
\]
(2) Record as \(k=\left(i, a^{*} c 1, b^{*} c 1+c 2\right)\)

\section*{Induction variable elimination: step 2}

For an induction variable \(k=(i, c 1, c 2)\)
(1) Initialize \(k=i^{*} c 1+c 2\) in the pre-header
(2) Replace \(k\) 's def in the loop by \(k=k+c 1\)
- Make sure to do this after i's definition

\section*{Outline}
- Simple loop transformations
- Loop invariants based transformations
- Induction variables based transformations
- Complex loop transformations

\section*{Loop transformations}
- Restructure a loop to expose more optimization opportunities and/or transform the "loop overhead"
- Loop unrolling, loop peeling, ...
- Reorganize a loop to improve memory utilization
- Cache blocking, skewing, loop reversal
- Distribute a loop over cores/processors
- DOACROSS, DOALL, DSWP, HELIX

\section*{Loop transformations for memory optimizations}
- How many clock cycles will it take?


\section*{Goal: improve cache performance}
- Temporal locality

A resource that has just been referenced will more likely be referenced again in the near future
- Spatial locality

The likelihood of referencing a resource is higher if a resource near it was just referenced
- Ideally, a compiler generates code with high temporal and spatial locality for the target architecture
- What to minimize: bad replacement decisions

\section*{What a compiler can do}
- Time:
- When is an object accessed?
- Space:
-Where does an object exist in the address space?
-What is the data layout of an object in memory?
- These are the two "knobs" a compiler can manipulate

\section*{First understand cache behavior ...}
- When do cache misses occur?
- Use locality analysis
- Can we change the visitation order to produce better behavior?
- Evaluate costs
- Does the new visitation order still produce correct results?
- Use dependence analysis
... and then rely on loop transformations
- loop interchange
- cache blocking
- loop fusion
- loop reversal
-...

\section*{Code example} double \(A[N][N], B[N][N]\); Iteration space for A
```

for i= 0 to N-1{
for j = 0 to N-1{
... = A[i][j] ...
}
} How can we represent the different memory accesses of
between all loop iterations?

```
```

A[0][1]

```
A[0][1]
```

A[0][0]

```
A[0][0]
A[1][0]
A[1][0]
A[1][1]
```

A[1][1]

```

\section*{Code example}
double \(A[N][N], B[N][N]\);
for \(\mathrm{i}=0\) to \(\mathrm{N}-1\{\)
for \(\mathrm{j}=0\) to \(\mathrm{N}-1\{\)
... \(=A[i][j] . .\).
\}
\}

Iteration space for A


Memory access performed at the iteration \(\mathrm{i}=0\) and \(\mathrm{j}=0\)

\section*{Code example}

\section*{double \(A[N][N], B[N][N]\);}
for \(\mathrm{i}=0\) to \(\mathrm{N}-1\{\)
for \(\mathrm{j}=0\) to \(\mathrm{N}-1\) \{
... \(=A[i][j] . .\).
\}
\(\}\)

Iteration space for A


Memory access performed at the iteration \(\mathrm{i}=0\) and \(\mathrm{j}=1\)

\section*{Code example}
```

double A[N][N], B[N][N];
for i= 0 to N-1{
for j = 0 to N-1{
... = A[i][j] ...
}
}

```

\section*{Iteration space for \(A\)}


\section*{Code example}
```

double A[N][N], B[N][N];
for i= 0 to N-1{
for j = 0 to N-1{
... = A[i][j] ...
}
}

```

\section*{Iteration space for \(A\)}

\[
\begin{gathered}
\text { for } i=0 \text { to } N-1 \\
\text { for } j=0 \text { to } N-1 \\
\ldots=A[j][i] \ldots
\end{gathered}
\]

Assumptions: N is large; A is row-major; 8 elements per cache line

0000000000000000000000 0000000000000000000000 0000000000000000000000 9000000000000000000000 0000000000000000000000 9000000000000000000000 0000000000000000000000 0000000000000000000000 0000000000000000000000 0000000000000000000000 0000000000000000000000
- Cache hit
(low \#cycles)
- Cache miss
(high \#cycles)
\[
\begin{gathered}
\text { for } i=0 \text { to } N-1 \\
\text { for } j=0 \text { to } N-1 \\
\ldots=A[j][i] \ldots
\end{gathered}
\]

\[
\begin{gathered}
\text { For } j=0 \text { to } N-1 \\
\text { for } i=0 \text { to } N-1 \\
\ldots=A[j][i] \ldots
\end{gathered}
\]

Assumptions: N is large; A is row-major; 8 elements per cache line
- Cache hit (low \#cycles)
- Cache miss
(high \#cycles)
i
0000000000000000000000 0000000000000000000000 0000000000000000000000 0000000000000000000000 0000000000000000000000 0000000000000000000000 0000000000000000000000 0000000000000000000000 0000000000000000000000 0000000000000000000000 \(0000000000000000000000 \rightarrow\)

\section*{Loop interchange}
for \(\mathrm{i}=0\) to \(\mathrm{N}-1\)
for \(\mathrm{j}=0\) to \(\mathrm{N}-1\)
\(\ldots=A[j][i] \ldots\)


Assumptions: N is large; A is row-major; 8 elements per cache line
- Cache hit (low \#cycles)
- Cache miss
(high \#cycles)
i
0000000000000000000000 9000000000000000000000
 0000000000000000000000 0000000000000000000000 \$000000000000000000000 0000000000000000000000 10000000000000000000000 0000000000000000000000 9000000000000000000000

\section*{Java (similar in C)}

To create a matrix:
double [][] \(\wedge=\) new double[3][3];

A is an array of arrays
\(A\) is not a 2 dimensional array!

\section*{Java (similar in C)}

To create a matrix:
double [][] A = new double[3][];
A[0] = new double[3];
A[1] = new double[3];
A[2] = new double[3];

\section*{Java (similar in C)}

To create a matrix:
double [][] A = new double[3][];
A[0] = new double[10];
A[1] = new double[5];
A[2] = new double[42];

A is a jagged array

\section*{C\#: [][] vs. [,]}
double [][] A = new double[3][]; A[0] = new double[3];
A[1] = new double[3];
A[2] = new double[3];
double [,] A = new double[3,3];


The compiler can easily choose between raw-major vs. column-major
```

1 \#include <stdio.h>
2
3 int main (){
4 int a[2][4];
5
6 printf("0x%p\n", \&a[0][0]);
7 printf("0x%p\n", \&a[0][1]);
8 printf(" Distance: %d bytes\n", ((unsigned int)(\&a[0][1])) - ((unsigned int)(\&a[0][0])));
9
10 printf("0x%p\n", \&a[0][0]);
11 printf("0x%p\n", \&a[1][0]);
12 printf(" Distance: %d bytes\n", ((unsigned int)(\&a[1][0])) - ((unsigned int)(\&a[0][0])));
13
1 4 ~ r e t u r n ~ 0 ; ~ ;
15}

```
\[
\begin{gathered}
\text { for } \mathrm{i}=0 \text { to } \mathrm{N}-1 \\
\text { for } \mathrm{j}=0 \text { to } \mathrm{N}-1 \\
\mathrm{f}(\mathrm{~A}[\mathrm{i}], A[\mathrm{j}])
\end{gathered}
\]

Assumptions: N is large; 8 elements per cache line

1000000000000000000000 0000000000000000000000 0000000000000000000000 .
- Cache hit (low \#cycles)
- Cache miss (high \#cycles)
\[
\begin{gathered}
\text { for } i=0 \text { to } N-1 \\
\text { for } j=0 \text { to } N-1 \\
f(A[i], A[j])
\end{gathered}
\]

Assumptions: N is large; 8 elements per cache line
i 100000000000000000000 000000000000000000000 0.000000000000000000000 0000000000000000000
- Cache hit (low \#cycles)
- Cache miss (high \#cycles)
\begin{tabular}{|c|}
\hline for \(i=0\) to \(N-1\) \\
for \(j=0\) to \(N-1\) \\
\(f(A[i], A[j])\)
\end{tabular}\(\quad\)\begin{tabular}{c} 
for \(J J=0\) to \(N-1\) by \(B\) \\
for \(i=0\) to \(N-1\) \\
for \(j=J J\) to min \((N-1, J J+B-1)\) \\
\(f(A[i], A[j])\)
\end{tabular}

Assumptions: N is large; 8 elements per cache line
- Cache hit (low \#cycles)
- Cache miss (high \#cycles)
i A000000000000000000000 0000000000000000000000 4000000000000000000000 0000000000000000000000
\begin{tabular}{|c|}
\hline for \(i=0\) to \(N-1\) \\
for \(j=0\) to \(N-1\) \\
\(f(A[i], A[j])\)
\end{tabular}\(\quad\)\begin{tabular}{c} 
for \(J J=0\) to \(N-1\) by \(B\) \\
for \(i=0\) to \(N-1\) \\
for \(j=J J\) to min \((N-1, J J+B-1)\) \\
\(f(A[i], A[j])\)
\end{tabular}

Assumptions: N is large; 8 elements per cache line
- Cache hit (low \#cycles)
- Cache miss (high \#cycles)
i A000000000000000000000 10000000000000000000000 1000000000000000000000 \(000000000000000000000 ; j\)

\section*{Loop fusion}

- Reduce loop overhead
- Improve locality by combining loops that reference the same array
- Increase the granularity of work done in a loop

\section*{Loop transformations}
- They manipulate the order of memory accesses
- They can change both temporal and spatial localities
- They can enable or disable parallelism

Always have faith in your ability

Success will come your way eventually

\section*{Best of luck!}```

